

**EAST Search History**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L4	3	((test tester tested testing) same ((first and second) near3 mode) same ((first and second) near3 (MUX SEL selector multiplexor multiplexer switch)) same ((first and second) near4 (logic block macro FPGA ASIC circuit array)) same (connection interconnect connect connecting connected interconnection)).clm. and ("714"/\$.ccls. "324"/\$.ccls.)	US-PGPUB	OR	ON	2007/07/20 09:49

PALM Intranet

Application Number

**IDS Flag Clearance for Application 10765895****IDS Information**

Content	Mailroom Date	Entry Number	IDS Review	Last Modified	Reviewer
WIDS	2004-01-29	14	Y <input checked="" type="checkbox"/>	2007-05-09 00:00:00.0	CR #232884
<input type="button" value="Update"/>					